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LVDSTL-1

Electrical and Mechanical Application Information

11/4/2010

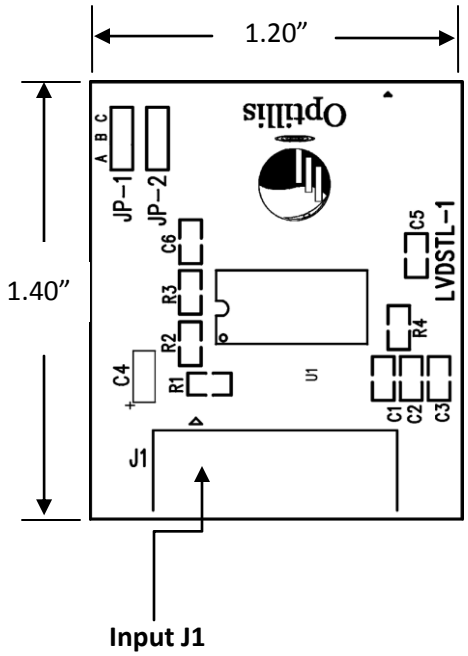
ClearClass LVDS to TTL Converter

The Optillis **ClearClass** LVDS to TTL converter is specifically designed to connect CMOS displays to 18bit LVDS transmitters. Our simplified design connects directly to the DF9-31S-1V Hirose connector on standard VGA displays.

Features and Advantages

- ✓ Direct connection to the display
- ✓ Low Noise Connectors
- ✓ Internal powered – no external supply needed
- ✓ Low Cost Design
- ✓ 3.3V Driving Voltage
- ✓ One Year Warranty

Dimensions



Connectors			
J1, LVDS Input **Connector:** Hirose DF19G-14P-1H	**J2**, Display Interface **Connector:** Hirose DF9-31S-1V		
1. Vcc, +3.3 volts	1. Ground	13. G0, Green LSB	25. B5, Blue MSB
2. Vcc, +3.3 volts	2. DCLK, Data Clock	14. G1	26. Gnd
3. Ground	3. HD Horizontal Sync	15. G2	27. DENA, Data
4. Ground	4. VD, Vertical Sync	16. G3	Enable Signal
5. RxIN0-	5. Ground	17. G4	28. Vcc, +3.3 volts
6. RxIN0+	6. R0, Red LSB	18. G5, Green MSB	29. Vcc, +3.3 volts
7. RxIN1-	7. R1	19. Ground	30. JP1-B
8. RxIN1+	8. R2	20. B0, Blue LSB	31, JP2-B
9. RxIN2-	9. R3	21. B1	
10. RxIN2+	10. R4	22. B2	
11. RxCLK-	11. R5, Red MSB	23. B3	
12. RxCLK+	12. Ground	24. B4	
13. Ground			
14. Ground			

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Jumpers

1) JP1, JP2 For display viewing options, default is open.
2) JP1-A, JP2-A: Vcc +3.3 volts
3) JP1-C, JP2-C: Ground
   Please refer to display data sheet for settings.

Mechanical Detail